

The listing of claims will replace all prior versions, and listings, of claims in the application:

Listing of Claims:

1. (Currently Amended) A mask blank for use in EUV lithography comprising a substrate with a front side and a rear side wherein the front side comprises a coating suitable for use as a mask in EUV lithography, and the rear side comprises an electrically conductive coating, wherein, with a layer thickness of about 100 nm, the ~~and a~~ resistivity of the electrically conductive coating is at least about $10^{-7} \Omega \text{ cm}$, wherein the electrically conductive coating has been applied by ion beam assisted deposition.
2. (Previously Presented) The mask blank according to claim 31, wherein the substrate comprises a material with an extremely low coefficient of thermal expansion.
3. (Previously Presented) The mask blank according to claim 31, wherein the electrically conductive coating has been applied by ion beam assisted sputtering.
4. (Previously Presented) The mask blank according to claim 3, wherein the resistance of the electrically conductive coating to abrasion with a cloth according to DIN 58196-5 (German Industry Standard) falls into at least category two.
5. (Previously Presented) The mask blank according to claim 3, wherein the resistance of the electrically conductive coating to abrasion with an eraser according to DIN 58196-4 (German Industry Standard) falls into at least category two.
6. (Previously Presented) The mask blank according to claim 3, wherein the adhesive strength of the electrically conductive coating determined in an adhesive tape test according to DIN 58196-6 (German Industry Standard) corresponds to a detachment of substantially 0%.

7. (Previously Presented) The mask blank according to claim 31, wherein the substrate comprises silica glass or ceramic glass.
8. (Previously Presented) The mask blank according to claim 31, wherein at least on the front side of the substrate a coating is applied which comprises a system of dielectric double layers.
9. (Previously Presented) The mask blank according to claim 8, wherein the dielectric double layers are applied by ion-beam-assisted deposition.
10. (Previously Presented) The mask blank according to claim 3, wherein the front side and the rear side have an identical coating.
11. (Currently Amended) A method for coating a mask blank for use in EUV lithography comprising
providing a substrate with a front side and a rear side;
applying a coating suitable for use as a mask in EUV lithography to the front side;
applying an electrically conductive coating by ion beam assisted deposition to the rear side, wherein, with a layer thickness of about 100 nm, the ~~and a~~ resistivity of the electrically conductive coating is at least about $10^{-7} \Omega \text{ cm}$.
12. (Previously Presented) The method according to claim 11, wherein the substrate comprises a material with an extremely low coefficient of thermal expansion.
13. (Previously Presented) The method according to claim 11, wherein the electrically conductive coating is applied by ion beam assisted sputtering.
14. (Previously Presented) The method according to claim 13, wherein the conductive coating is applied in such a way that the resistance of the electrically conductive coating to abrasion with a cloth according to DIN 58196-5 (German Industry Standard) falls into at least category two.

15. (Previously Presented) The method according to claim 13, wherein the conductive coating is applied in such a way that the resistance of the electrically conductive coating to abrasion with an eraser according to DIN 58196-4 (German Industry Standard) falls into at least category two.

16. (Previously Presented) The method according to 13, wherein the conductive coating is applied in such a way that the adhesive strength of the electrically conductive coating determined in an adhesive tape test according to DIN 58196-6 (German Industry Standard) corresponds to a detachment of substantially 0%.

17. (Previously Presented) The method according to claim 11, in which at least on the front side of the substrate a coating is applied which comprises a system of dielectric double layers.

18. (Previously Presented) The method according to claim 17, wherein the dielectric double layers are applied by ion-beam-assisted sputtering.

19. (Previously Presented) The method according to claim 13, in which the front side and the rear side have an identical coating.

20. (Previously Presented) The mask blank according to claim 3, wherein the electrically conductive coating has been applied by
sputtering a target by irradiating with a first particle beam to thereby sputter the electrically conductive coating onto the rear side of the substrate, and
flattening the electrically conductive coating by irradiating with a second particle beam after the deposition of the electrically conductive coating.

21. (Previously Presented) The mask blank according to claim 20, wherein the rear side of the substrate has been cleaned from impurities by irradiating with the second particle beam before the electrically conductive coating is sputtered onto the rear side of the substrate.

22. (Previously Presented) The mask blank according to claim 21, wherein the substrate and the target have been disposed in a vacuum chamber, at least one reactive gas

has been provided in the vacuum chamber at a predetermined pressure, wherein cleaning of the rear side of the substrate has been enhanced by the at least one reactive gas.

23. (Previously Presented) The mask blank according to claim 22, wherein the at least one reactive gas comprises oxygen.

24. (Previously Presented) The method according to claim 13, wherein the electrically conductive coating is applied by

sputtering a target by irradiating with a first particle beam to thereby sputter the electrically conductive coating onto the rear side of the substrate; and

flattening the electrically conductive coating by irradiating with a second particle beam after the deposition of the electrically conductive coating.

25. (Previously Presented) The method according to claim 24, further comprising cleaning the rear side of the substrate from impurities by irradiating with the second particle beam before the electrically conductive coating is sputtered onto the rear side of the substrate.

26. (Previously Presented) The method according to claim 25, further comprising
disposing the substrate and the target in a vacuum chamber, and providing at least one reactive gas in the vacuum chamber at a predetermined pressure;
wherein cleaning of the rear side of the substrate has been enhanced by the at least one reactive gas.

27. (Previously Presented) The method according to claim 26, wherein the at least one reactive gas comprises oxygen.

28. (Previously Presented) The mask blank according to claim 8, wherein the system of dielectric double layers comprises Mo/Si double layers, and one chromium layer or one EUV-absorbing layer.

29. (Previously Presented) The mask blank according to claim 31, wherein the electrically conductive coating has a resistivity of at least about $10^{-6} \Omega \text{ cm}$.

30. (Previously Presented) The mask blank according to claim 31, wherein the electrically conductive coating has a resistivity of at least about $10^{-5} \Omega \text{ cm}$.

31. (Currently Amended) A mask blank for use in EUV lithography comprising a substrate with a front side and a rear side wherein the front side comprises a coating suitable for use as a mask in EUV lithography, and the rear side comprises an electrically conductive coating, wherein, with a layer thickness of about 100 nm, a the resistivity of the electrically conductive coating is at least about $10^{-7} \Omega \text{ cm}$, and a homogeneity of peak reflection of smaller than $\pm 1\%$ and a homogeneity of a center wavelength of smaller than $\pm 0.1 \text{ nm}$ over the whole area of the mask blank, wherein the electrically conductive coating has been applied by ion beam assisted deposition.

32. (Previously Presented) A mask blank according to claim 31, wherein the electrically conductive coating has a homogeneity of peak reflection of smaller than $\pm 0.2\%$ and a homogeneity of a center wavelength of smaller than $\pm 0.02 \text{ nm}$ over the whole area of the mask blank.

33. (Previously Presented) A mask blank according to claim 31, wherein the electrically conductive coating has a resistivity of at between about $10^{-5} \Omega \text{ cm}$ and $10^{-7} \Omega \text{ cm}$.